

TX-M2541 Datasheet

Zigbee + BLE5.0 Combo Module



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Revision History

Revision	Date	Description			
0.1	2022.04.08	Initial release			
0.2	2022.08.11	I.Add Module internal PCB antenna specification			
		2.Modify USB			
0.3	2022.08.22	Add Coordinate in Physical Dimensions			
1.0	2022.12.16	Modify Packing Information			



Table of Contents

1	Scope	. 5
	·	
2.	Features	٠5
3.	Block Diagram ·····	. 6
4.	Product Information	. 7
5.	Pin Description	. 7
6.	Electrical Specification	. 9
7.	RF Specification ·····	11
8.	Physical Dimensions	12
9.	Module Internal PCB Antenna Specification	13
10.	SMT Temperature Sequence (Pb-free) ······	15
11.	Packing Information ·····	16



1. Scope

The TX-M2541 is Bluetooth LE + IEEE802.15.4 multi-standard wireless solution with internal Flash and audio support, which combines the features and functions needed for all 2.4GHz IoT standards into a module. The TX-M2541 combines the radio frequency (RF), digital processing, protocols stack software and profiles for multiple standards into a module. The module supports standards and industrial alliance specifications including Bluetooth Low Energy (up to Bluetooth 5), BLE Mesh, 6LoWPAN, Zigbee, RF4CE, HomeKit and 2.4GHz proprietary standard.

Application:

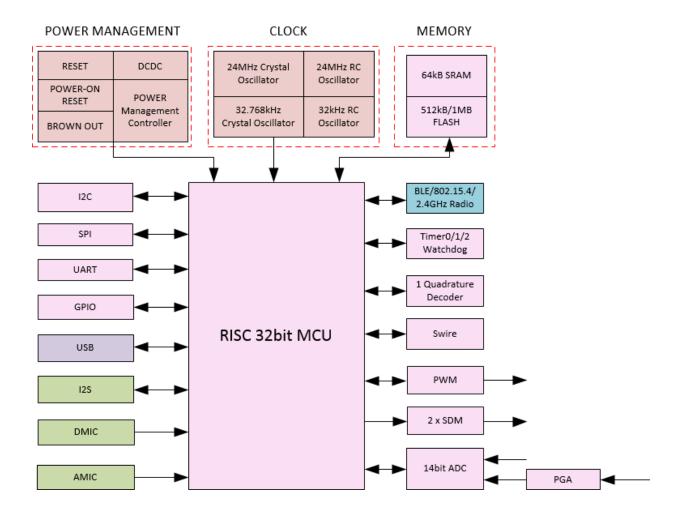
- Smartphone and tablet accessories
- RF Remote control
- Sports and fitness tracking
- Wearable devices

2. Features

- Embedded32-bit high performance MCU with clock up to 48MHz.
- Program memory: internal 512KB/1M Flash
- Data memory: 64KB on-chip SRAM.
- 24MHz & 32.768KHz Crystal and 32KHz/24MHz embedded RC oscillator.
- Up to +10dBm TX power.
- RX sensitivity: -96 dBm @ BLE 1 Mbps, -99.5 dBm @ IEEE 802.15.4 250 kbps mode
- Up to 16 GPIOs
- DMIC (Digital Mic).
- AMIC (Analog Mic)
- Stereo audio output.
- UART with hardware flow control
- SPI/ I2C/ I2S/ Debug Interface.
- Up to 6 channels of PWM, 1-channel IR.
- Sensor: 14-bit 10-channel (only GPIO input) SAR ADC, with 4-channel differential input PGA/Temperature sensor.
- One quadrature decoder.
- Embedded hardware AES.



3. Block Diagram





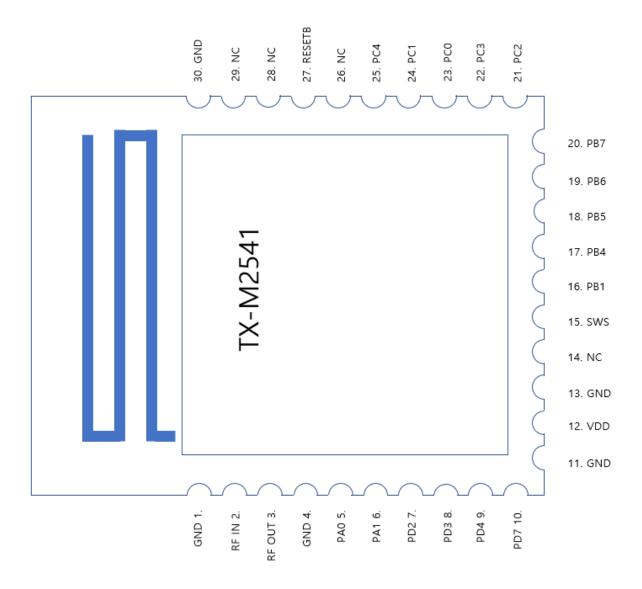
4. Product Information

4.1 Temperature Information

- Operating temperature	-40°C ~ +85°C
- Storage temperature	-40℃ ~ +125℃

5. Pin Description

TOP View





Pin	Name	Туре	Description		
1	GND	GND	Ground		
2	RF IN	RF_IN	RF input		
3	RF OUT	RF_OUT	RF output		
4	GND	GND	Ground		
5	PA0	I/O	DMIC data input / PWM0 inverting output / UART_RX / GPIO PA[0]		
6	PA1	I/O	DMIC clock / UART7816 clock / I2S clock / GPIO PA[1]		
7	PD2	I/O	SPI chip select (Active low) / I2S left right channel select / PWM3 output / GPIO PD[2] /		
8	PD3	I/O	PWM1 inverting output / I2S serial data input / UART7816TRX (UART_TX) / GPIO PD[3]		
9	PD4	I/O	Single wire master / I2S serial data output / PWM2 inverting output / GPIO PD[4]		
10	PD7	I/O	SPI clock(I2C_SCK) / I2S bit clock / UART7816 TRX (UART_TX) / GPIO PD[7]		
11	GND	GND	Ground		
12	VDD	Vdd	3.3V Power Supply		
13	GND	GND	Ground		
14	NC	NC	Not Connect		
15	SWS	SWS	Single Wire Slave		
16	PB1	I/O	PWM4 output / UART_TX / Antenna select pin2		
			/ Low power comparator input / SAR ADC input / GPIO PB[1]		
17	PB4	I/O	SDM positive output 0 / PWM4 output / Low power comparator input/ SAR ADC input / GPIO PB[4]		
18	PB5	I/O	SDM negative output 0 / PWM5 output / Low power comparator Input / SAR ADC input / GPIO PB[5]		
19	PB6	I/O	SDM positive output 1 / SPI data input(I2C_SDA) / UART_RTS / Low power comparator input / SAR ADC input / GPIO PB[6]		
20	PB7	I/O	SDM negative output 1 / SPI data output / UART_RX / Low power Comparator input / SAR ADC input / GPIO PB[7]		
21	PC2	I/O	PWM0 output / UART 7816 TRX(UART_TX) / I2C serial data / 32KHz crystal output / PGA right channel positive input / GPIO PC[2]		
22	PC3	I/O	PWM1 output / UART_RX/I2C serial clock / 32KHz crystal input / PGA right channel negative input / GPIO PC[3]		
23	PC0	I/O	I2C serial data / PWM4 inverting output / UART_RTS / PGA left channel positive input / GPIO PC[0] t		
24	PC1	I/O	I2C serial clock / PWM1 inverting output / PMW0 output / PGA left channel negative input / GPIO PC[1]		



25	PC4	I/O	PWM2 output / UART_CTS / PWM0 inverting output /
23		1/0	SAR ADC input / GPIO PC[4]
26	NC	NC	Not Connect
27	RESETB	Reset	Power on reset, Active low
28	NC	NC	Not Connect
29	NC	NC	Not Connect
30	GND	GND	Ground

6. Electrical Specification

6.1 Absolute Maximum Rating

Item	Min	Max	Unit
Supply Voltage	-0.3	3.6	V
Voltage on input Pin	-0.3	VDD+0.3	V
Output Voltage	0	VDD	V
Storage temperature Range	-65	150	℃

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

6.2 Recommended Operating condition

Item	Min	Тур	Max	unit	Condition
Power Supply Voltage	1.8	3.3	3.6	V	
Supply rise time (from 1.6V to 2.8V)			10	ms	
Operating temperature range		-40	85	°C	

6.3 Current Consumption

Item	Min	Тур	Max	unit	Condition
Тх	ı	4.8	-	mA	Whole chip @ 0 dBm with DCDC
Rx	-	5.3	-	mA	Whole chip
Deep sleep with 8 KB SRAM retention	-	1.0	3.1	uA	
Deep sleep with 16 KB SRAM retention	-	1.2	3.3	uA	Without 32K
Deep sleep with 32KB SRAM retention	-	1.4	3.5	uA	without 32K
Deep sleep without SRAM retention	-	0.4	-	uA	



6.4 AC characteristics

6.4.1 Digital inputs/outputs

Item	Min	Тур	Max	unit	Condition
Input high voltage	0.7VDD	-	VDD	V	
Input low voltage	VSS	-	0.3VDD	V	
Output high voltage	0.9VDD	-	VDD	V	
Output low voltage	VSS	-	0.1VDD	V	



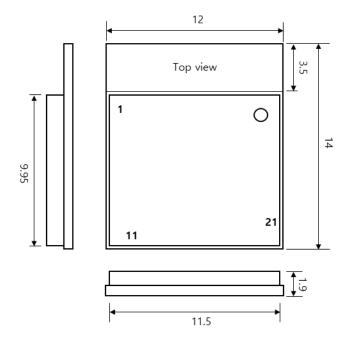
7. RF Specification

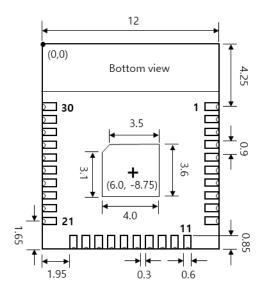
Nomal Condition : T=25°C, VDD=3.3V

Itei	Min	Тур	Max	unit	Condition			
RF frequency range	2380		2500	MHz	Programmable in 1MHz step			
		BLE/2.4G proprietary 1Mbps, ±250kHz deviation						
	BLE/2.4G proprietary 2Mbps, ±500kHz deviation							
		BLE 125k	BLE 125kbps, ±250kHz deviation					
Data rate		BLE 500k	bps, ±250l	kHz devia	tion			
		IEEE 802.	15.4 250kb	ps, ±500	kHz devia	ation		
		2.4G prop	rietary 500	kbps, ±12	25kHz dev	viation		
		2.4G prop	rietary 250	kbps, ±62	2.5kHz de	viation		
	BLE 1Mbps F	RF_Rx Perf	ormance (±250kHz	Deviation	n)		
Sensitivity	1Mbps		-96		dBm			
Frequency offset tolera	ance	-250		+300	kHz			
Co-channel rejection			11		dB	Wanted signal at -67dBm		
In-band blocking	+1/-1MHz offset		-1/-3		dB			
rejection	+2/-2MHz offset		-37/-39		dB	Wanted signal at -67dBm		
(equal modulation interference)	≥3MHz offset		-42		dB	Ū		
Image rejection	1		-37		dB	Wanted signal at -67dBm		
	Bl	E 1Mbps R	RF_Tx Perf	ormance	•	,		
Output power, maximu	um setting		10	12	dBm			
Output power, minimu	m setting		-45		dBm			
Programmable output	power range		55		dB			
Modulation 20dB band	dwidth		1.4		MHz			
	IEEE 802.15.4 250	(bps RF_R	Performa	nce (±50	0kHz Dev	viation)		
Sensitivity	250kbps		-99.5		dBm			
Frequency offset tolera	ance	-300		+300	kHz			
Adjacent channel reject (-1/+1 channel)	ction		-42/-42		dB	Wanted signal at -82dBm		
Adjacent channel reject	ction		-42/-42		٩D	Wantad aignal at 92dPm		
(-2/+2 channel)			-42/-42		dB	Wanted signal at -82dBm		
IEEE 802.15.4 250kbps RF_Tx Performance								
Output power, maximu		10	12	dBm				
Output power, minimu (resolution)		-45		dBm				
Programmable output		55		dB				
Modulation 20dB band	dwidth		2.7		MHz			
Error vector magnitude	e (EVM)			2	%	Max(10dBm) power output		



8. Physical Dimensions (Unit:mm)







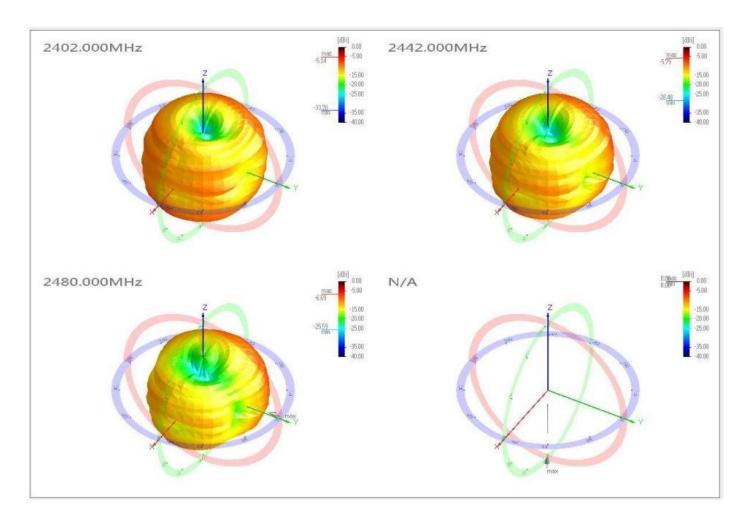
9. Module Internal PCB Antenna Specification

9.1 Antenna 2D Radiation Pattern



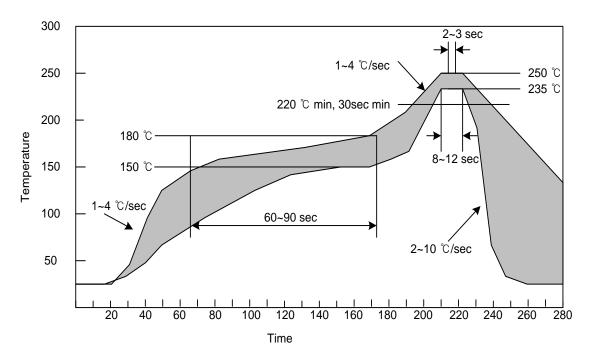


9.2 Antenna 3D Radiation Pattern





10. SMT Temperature Sequence (Pb-free)

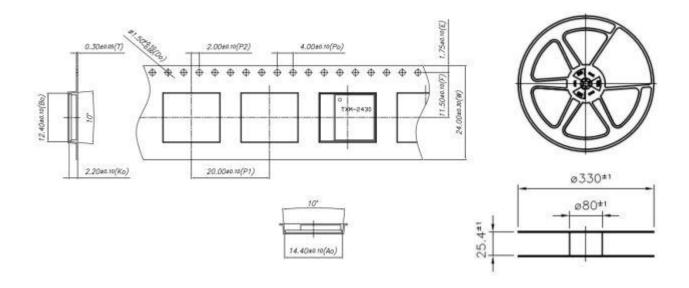


Process	Parmeter	Data		
	Conveyer Speed	min 0.8m/min max		
	conveyer opeca	0.95m/min		
	02 농도	3000 ppm以下		
Reflow Profile	Pre-Heating	150~180℃[60~90sec]		
	Heating	220℃[30~60sec]		
	Peak	235~250℃		



11. Packing Information

11.1 Carrier Tape and Reel Information



11.2 Leader and Trailer length

